

THE COMMISSIONER FOR PATENTS, Washington, D.C. 20231

Enclosed for filing is the patent application of Inventor:
JOHN PETRUZZELLO, THEODORE LETAVIC and MARK SIMPSON

FOR: HV-SOI LDMOS DEVICE WITH INTEGRATED DIODE TO IMPROVE
RELIABILITY AND AVALANCHE RUGGEDNESS

ENCLOSED ARE:

- ☒ Appointment of Associates;
- ☒ Information Disclosure Statement, Form PTO-1449 and copies of documents listed therein;
- ☐ Preliminary Amendment;
- ☒ Specification (13 Pages of Specification, Claims, & Abstract);
- ☒ Declaration and Power of Attorney:
(2 Pages of a ☒ fully executed ☐ unsigned Declaration);
- ☒ Drawing (4 sheets of ☒ informal ☐ formal sheet);
- ☐ Certified copy of Application No. ;
- ☒ Authorization Pursuant to 37 CFR §1.136(a)(3)
- ☐ Other;
- ☒ Assignment to KONINKLIJKE PHILIPS ELECTRONICS N.V.

**FEE COMPUTATION
CLAIMS AS FILED**

FOR	NUMBER FILED	NUMBER EXTRA	RATE	BASIC FEE - \$710.00
Total Claims	18 - 20 =		X \$18 =	
Independent Claims	4 - 3 =	1	X \$80 =	80.00
Multiple Dependent Claims, if any			\$270 =	
TOTAL FILING FEE				= \$ 790.00

Please charge Deposit Account No. 14-1270 in the amount of the total filing fee indicated above, plus any deficiencies. The Commissioner is also hereby authorized to charge any other fees which may be required, except the issue fee, or credit any overpayment to Account No. 14-1270.

☐ Amend the specification by inserting before the first line as a centered heading --Cross Reference to Related Applications--; and insert below that as a new paragraph --This is a continuation-in-part of application Serial No. , filed , which is herein incorporated by reference--.

CERTIFICATE OF EXPRESS MAILING


Express Mail Mailing Label No. EL 666 160 287 US

Date of Deposit: June 28, 2001

I hereby certify that this paper and/or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. 1.10 on the date indicated above and is addressed to the Commissioner of Patents and Trademarks, Washington, D.C. 20231.

Patti DeMichele
Typed Name

Signature


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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of
JOHN PETRUZZELLO ET AL

Atty. Docket
US 010320



Serial No.

Filed: CONCURRENTLY

HV-SOI LDMOS DEVICE WITH INTEGRATED DIODE TO IMPROVE RELIABILITY
AND AVALANCHE RUGGEDNESS

Commissioner for Patents
Washington, D.C. 20231

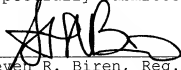
AUTHORIZATION PURSUANT TO 37 CFR §1.136(a)(3)
AND TO CHARGE DEPOSIT ACCOUNT

Sir:

The Commissioner is hereby requested and authorized to treat any concurrent or future reply in this application requiring a petition for extension of time for its timely submission, as incorporating a petition for extension of time for the appropriate length of time.

Please charge any additional fees which may now or in the future be required in this application, including extension of time fees, but excluding the issue fee unless explicitly requested to do so, and credit any overpayment, to Deposit Account No. 14-1270.

Respectfully submitted,

By 
Steven R. Biren, Reg.26,531
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